

Abstract of the Disclosure:

A circuit configuration for generating a control signal for an engine control unit, designed to control at least one fuel injector of an internal combustion engine, enables an improved control signal course during the control of the injectors. The configuration includes: a counter device, to which a predefined clock signal can be supplied, for providing a time-dependent digital counter signal, based on the counting of the clock signal; a memory unit, in which the digital counter signal is entered, for storing a series of digital control signal values and for the successive issue of individual control signal values from the series of control signal values, in accordance with the counter signal; and a digital-to-analog converter for converting the issued digital control signal values into the analog control signal for the engine control unit.